

[54] MEMORY READ/WRITE ORGANIZATION FOR A TELEVISION SIGNAL PROCESSOR

[75] Inventors: Robert Sherman Hopkins, Jr., Marlton; Arthur James Banks, Cherry Hill; Robert Adams Dischert, Burlington, all of N.J.

[73] Assignee: RCA Corporation, New York, N.Y.

[21] Appl. No.: 755,949

[22] Filed: Dec. 30, 1976

[30] Foreign Application Priority Data

Mar. 19, 1976 [GB] United Kingdom 11113/76

[51] Int. Cl.² H04N 5/78; H04N 9/46

[52] U.S. Cl. 358/4; 358/19

[58] Field of Search 358/4, 8, 22, 17, 19, 358/149; 360/10, 11, 32, 33, 35, 36, 38

[56] References Cited

U.S. PATENT DOCUMENTS

3,564,123 2/1971 Pezirtoglov 360/10
 3,890,638 6/1975 Barga 358/8

OTHER PUBLICATIONS

Stratton, "Reviewing Slow-Motion Disc Principles", Broadcast Engineering, Feb. 1969, pp. 14-18.

Primary Examiner—John C. Martin
 Attorney, Agent, or Firm—Eugene M. Whitacre; Paul J. Rasmussen; Mason DeCamillis

[57] ABSTRACT

In a television signal synchronizer having a coherent memory, it is necessary to modify the writing of data into memory or the reading of data out of memory in order to provide field conversion. In the described apparatus, the reading out of data is always done according to a non-variant sequence determined by the local (studio) reference timing signals. In a first embodiment the read address generator signals are modified so as to read out the stored video signal in accordance with the non-variant write in sequence. In a second embodiment the write address generator signals are modified so as to position the incoming video signal into memory so that it is located in the proper memory position for the non-variant read out sequence.

3 Claims, 21 Drawing Figures

